

**U.S. Serial No. 09/729,010**  
**Response to the Office Action of November 18, 2004**

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Previously presented) A method for storing and retrieving digital data within a hardware platform, the method comprising:

- receiving data bits across a bus, the data bits forming a bit pattern;
- altering the bit pattern of the data bits;
- storing the altered data bits;
- restoring the altered data bits to the bit pattern; and
- outputting the restored data bits, wherein

the altering comprises one of inverting bits in selected bit positions of the data bits and scrambling bits in the selected bit positions of the data bits to prevent unauthorized use of the data bits.

2-4. (Canceled).

5. (Previously presented) The method according to claim 1, wherein the altering and the restoring are performed by a hard disk drive interface.

6. (Previously presented) The method according to claim 1, wherein the altering is unique to the hardware platform.

**U.S. Serial No. 09/729,010**  
**Response to the Office Action of November 18, 2004**

7. (Previously presented) The method according to claim 1, wherein the altering is unique to a plurality of hardware platforms.

8. (Previously presented) The method according to claim 1, wherein the altering is based upon a serial number of the hardware platform.

9. (Previously presented) The method according to claim 1, further comprising:  
generating a random number upon power-up of the hardware platform,  
wherein the altering is based upon the random number.

10-11. (Canceled).

12. (Previously presented) An apparatus for storing and retrieving digital video data, comprising:

a system bus configured to transfer data bits, the data bits forming a bit pattern;

an interface coupled to the system bus and configured to alter the bit pattern of the data bits; and

a hard disk drive coupled to the interface and configured to store the altered data bits,

wherein

the interface is configured to alter the bit pattern by one of inverting bits in selected bit positions of the data bits and scrambling bits in the selected bit positions of the data bits to prevent unauthorized use of the data bits.

13-16. (Canceled).

**U.S. Serial No. 09/729,010**

**Response to the Office Action of November 18, 2004**

17. (Previously presented) The apparatus according to claim 12, wherein the interface is further configured to alter the bit pattern of the data bits in a manner that is unique to the apparatus.

18. (Previously presented) The apparatus according to claim 12, wherein the interface is further configured to alter the bit pattern of the data bits in a manner that is unique to a plurality of apparatuses.

19. (Previously presented) The apparatus according to claim 12, wherein the interface is further configured to alter the bit pattern of the data bits based upon a serial number of the apparatus.

20. (Previously presented) The apparatus according to claim 12, further comprising:  
a processor coupled to the system bus and configured to generate a random number during power-up of the apparatus,

wherein the interface is further configured to alter the bit pattern of the data bits based upon the random number.

21-32. (Canceled).

**U.S. Serial No. 09/729,010**  
**Response to the Office Action of November 18, 2004**

33. (Previously presented) A computer-readable medium carrying one or more sequences of one or more instructions for storing and retrieving digital video data within a hardware platform, the one or more sequences of one or more instructions including instructions which, when executed by one or more processors, cause the one or more processors to perform:

receiving data bits across a bus, the data bits forming a bit pattern;

altering the bit pattern of the data bits by one of inverting bits in selected bit positions of the data bits and scrambling bits in the selected bit positions of the data bits to prevent unauthorized use of the data bits;

storing the altered data bits;

restoring the altered data bits to the bit pattern; and

outputting the restored data bits.

34-36. (Canceled).

37. (Previously presented) The computer-readable medium according to claim 33, wherein the altering and the restoring are performed by a hard disk drive interface.

38. (Previously presented) The computer-readable medium according to claim 33, wherein the altering is unique to the hardware platform.

39. (Previously presented) The computer-readable medium according to claim 33, wherein the altering is unique to a plurality of hardware platforms.

**U.S. Serial No. 09/729,010**  
**Response to the Office Action of November 18, 2004**

40. (Previously presented) The computer-readable medium according to claim 33, wherein the altering is based upon a serial number of the hardware platform.

41. (Previously presented) The computer-readable medium according to claim 33, wherein the one or more processors further perform:

generating a random number upon power-up of the hardware platform,  
wherein the altering is based upon the random number.

42. (Previously presented) The computer-readable medium according to claim 33, wherein the altered data bits are stored on a hard disk drive.

43. (Canceled).

44. (Previously presented) An apparatus for storing and retrieving digital video data within a hardware platform, the apparatus comprising:

means for receiving data bits across a bus, the data bits forming a bit pattern;

means for altering the bit pattern of the data bits by one of inverting bits in selected bit positions of the data bits and scrambling bits in the selected bit positions of the data bits to prevent unauthorized use of the data bits;

means for storing the altered data bits;

means for restoring the altered data bits to the bit pattern; and

means for outputting the restored data bits.

45-47. (Canceled)

**U.S. Serial No. 09/729,010**  
**Response to the Office Action of November 18, 2004**

48. (Previously presented) The apparatus according to claim 44, wherein the means for altering the bit pattern of the data bits is configured to alter the data bits in a manner that is unique to the hardware platform.

49. (Previously presented) The apparatus according to claim 44, wherein the means for altering the bit pattern of the data bits is configured to alter the data bits in a manner that is unique to a plurality of hardware platforms.

50. (Previously presented) The apparatus according to claim 44, wherein the means for altering the bit pattern of the data bits is configured to alter the data bits based upon a serial number of the hardware platform.

51. (Previously presented) The apparatus according to claim 44, further comprising:  
means for generating a random number upon power-up of the hardware platform,  
wherein the means for altering the bit pattern of the data bits is configured to alter the data bits based upon the random number.

52-53. (Canceled)

**U.S. Serial No. 09/729,010**  
**Response to the Office Action of November 18, 2004**

54. (Previously presented) A method for storing and retrieving digital data within a hardware platform, the method comprising:

receiving a plurality of data bits, the data bits forming a bit pattern;  
altering the bit pattern by inverting bits in a first selection of bit positions of the data bits;  
storing the altered bit pattern on a medium;  
retrieving the stored altered bit pattern from the medium;  
restoring the altered bit pattern by inverting the bits of the first selection of bit positions of the retrieved bit pattern; and  
outputting the restored bit pattern.

55. (Previously presented) The method of claim 54, wherein the first selection of bit positions are unique to the hardware platform.

56. (Previously presented) The method of claim 54, wherein the first selection of bit positions are based on a serial number of the hardware platform.

57. (Previously presented) The method of claim 54, wherein the first selection of bit positions are based on a random number.

58. (Previously presented) The method of claim 54, wherein:  
the altering further comprises scrambling bits of a second selection of bit positions of the bit pattern, and  
the restoring further comprises unscrambling the bits of the second selection of bit positions of the retrieved altered bit pattern.

**U.S. Serial No. 09/729,010**  
**Response to the Office Action of November 18, 2004**

59. (Previously presented) A method for storing and retrieving digital data within a hardware platform, the method comprising:

- receiving a plurality of data bits, the data bits forming a bit pattern;
- altering the bit pattern by scrambling bits of selected bit positions of the data bits;
- storing the altered bit pattern on a medium;
- retrieving the stored altered bit pattern from the medium;
- restoring the altered bit pattern by unscrambling the bits of the selected bit positions of the retrieved bit pattern; and
- outputting the restored bit pattern.

60. (Previously presented) The method of claim 59, wherein the selected bit positions are unique to the hardware platform.

61. (Previously presented) The method of claim 59, wherein the selected bit positions are based on a serial number of the hardware platform.

62. (Previously presented) The method of claim 59, wherein the selected bit positions are based on a random number.



**U.S. Serial No. 09/729,010**  
**Response to the Office Action of November 18, 2004**

63. (Previously presented) A method for storing and retrieving digital data within a media receiver, the method comprising:

receiving a plurality of data bits from a service provider, the data bits forming a bit pattern;

altering the bit pattern without receiving from the service provider a control signal indicative of a characteristic associated with storing the bit pattern on a medium;

storing the altered bit pattern on the medium;

retrieving the stored altered bit pattern from the medium;

restoring the altered bit pattern; and

outputting the restored bit pattern.

64. (Previously presented) A method as defined in claim 63, wherein altering the bit pattern comprises inverting at least one bit in a selected bit position of the data bits via at least one inverter.

65. (Previously presented) A method as defined in claim 63, wherein altering the bit pattern comprises:

receiving the plurality of data bits from a bus, each of the plurality of data bits corresponding to one of a plurality of lines associated with the bus;

selecting at least one of the plurality of lines to form a line selection; and

inverting at least one of the plurality of data bits based on the line selection.

**U.S. Serial No. 09/729,010**  
**Response to the Office Action of November 18, 2004**

66. (Previously presented) A method as defined in claim 65, wherein selecting the at least one of the plurality of lines to form the line selection comprises selecting the at least one of the plurality of lines based on at least one of a random number and a serial number associated with the media receiver.

67. (Previously presented) A method as defined in claim 65, wherein selecting the at least one of the plurality of lines to form the line selection comprises controlling at least one of a plurality of multiplexers to form the line selection, and wherein each of the plurality of multiplexers corresponds to one of the plurality of lines.

68. (Previously presented) A method as defined in claim 65, wherein each of the plurality of lines is associated with an inverter.

69. (Previously presented) A method as defined in claim 63, wherein the media receiver comprises at least one of a tuning device, a digital video recording device, and a personal computer.